

3588 U.S. PTO  
10/14507

926

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10014507	12/14/2001	438	34	2322 242	Schilling

**\*\*APPLICANTS:** Tsukiji Naoki; Irino Satoshi; *ok*

**\*\*CONTINUING DATA VERIFIED:** *ok*

**\*\* FOREIGN APPLICATIONS VERIFIED:** *ok*  
JAPAN 2001-206994 07/06/2001

PG-PUB <input type="checkbox"/>	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		214144US8
Verified and Acknowledged Examiners's initials		
TITLE : Method of manufacturing semiconductor element, and semiconductor element manufactured with this method		

U.S. DEPT. OF COMMERCE/PAT. & TM-PTO-436L (Rev. 12-94)

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drawg.	Figs. Drawg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
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